

OSA20N60S / OSK20N60S 600V N-Channel MOSFET

Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

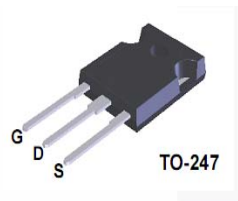
This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion inswitching mode operation for higher efficiency.

Features

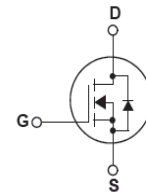
- 650V @TJ = 150 °C
- Typ. RDS(on) = 0.16 Ω
- Ultra Low Gate Charge (typ. Qg = 63nC)
- 100% avalanche tested
- Rohs Compliant



TO-3P



TO-247



Absolute Maximum Ratings

Symbol	Parameter	OSA-K20N60S	Unit
V _{DSS}	Drain-Source Voltage	600	V
I _D	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	20* 10*	A
I _{DM}	Drain Current - Pulsed (Note 1)	62*	A
V _{GSS}	Gate-Source voltage	±30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	525	mJ
I _{AR}	Avalanche Current (Note 1)	20	A
E _{AR}	Repetitive Avalanche Energy (Note 1)	1	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns
P _D	Power Dissipation (TC = 25°C) -Derate above 25°C	151 1.67	W W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	°C

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	OSA-K20N60S	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.83	°C/W
R _{θCS}	Thermal Resistance, Case-to-Sink Typ.	0.5	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62	°C/W

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Electrical Characteristics TC = 25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25 °C	600	--	--	V
		V _{GS} = 0V, I _D = 250μA, T _J = 150 °C	--	650	--	V
Δ BV _{DSS} / Δ T _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25 °C	--	0.6	--	V/°C
ID _{SS}	Zero Gate Voltage Drain Current	V _{DS} = 600V, V _{GS} = 0V V _{DS} = 480V, TC = 125 °C	--	--	1 10	μA μA
IG _{TSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	--	--	100	nA
IG _{SSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.5	--	4.5	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 10A	--	0.16	0.19	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40V, I _D = 5A (Note 4)	--	16	--	S
R _g	Gate Resistance	F=1MHz, open drain	--	4.5	--	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	--	1440	--	pF
C _{oss}	Output Capacitance		--	300	--	pF
C _{rss}	Reverse Transfer Capacitance		--	10	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 400V, I _D = 10A R _G = 20 Ω (Note 4, 5)	--	25	--	ns
t _r	Turn-On Rise Time		--	55	--	ns
t _{d(off)}	Turn-Off Delay Time		--	70	--	ns
t _f	Turn-Off Fall Time		--	40	--	ns
Q _g	Total Gate Charge	V _{DS} = 480V, I _D = 20A V _{GS} = 10V (Note 4, 5)	--	63	--	nC
Q _{gs}	Gate-Source Charge		--	7.8	--	nC
Q _{gd}	Gate-Drain Charge		--	9	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	20	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	60	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 20A	--	--	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 20A diF/dt = 100A/μs (Note 4)	--	475	--	ns
Q _{rr}	Reverse Recovery Charge		--	5.8	--	μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=10.5mH, I_{AS}=10A, V_{DD}=150V, Starting T_J=25 °C
3. I_{SD} ≤ 20A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25 °C
4. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%
5. Essentially Independent of Operating Temperature Typical Characteristics

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Typical Performance Characteristics

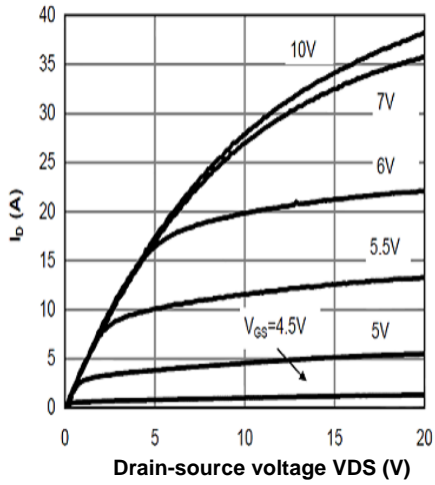


Figure 1: On-Region Characteristics @ 25° C

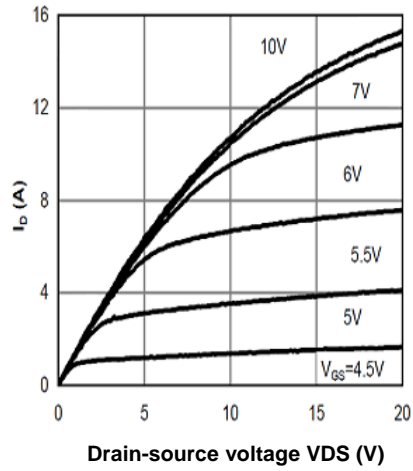


Figure 2: On-Region Characteristics @ 125° C

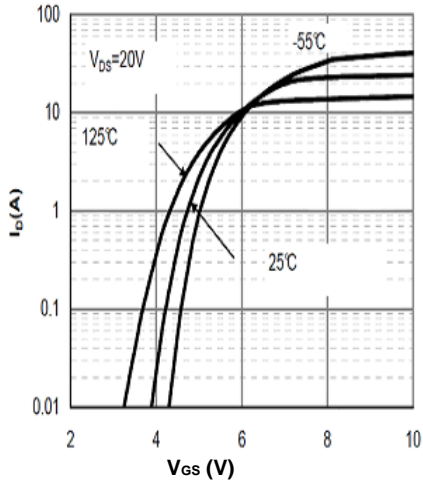


Figure 3: Transfer Characteristics

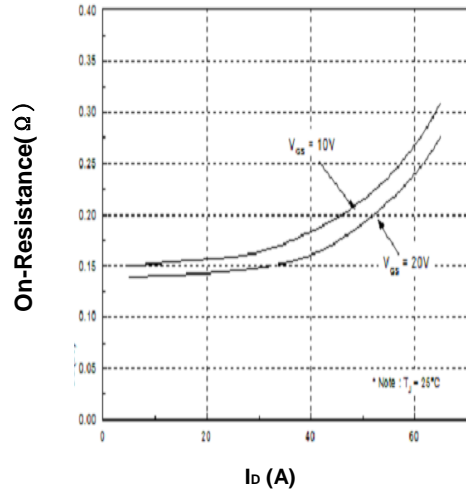


Figure 4: On-Resistance vs. Drain Current (I_b)

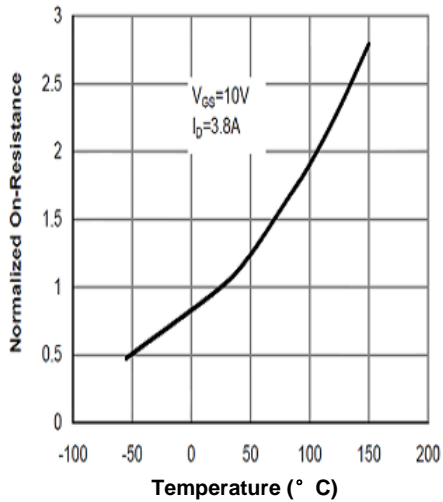


Figure 5: On-Resistance vs. Junction Temperature

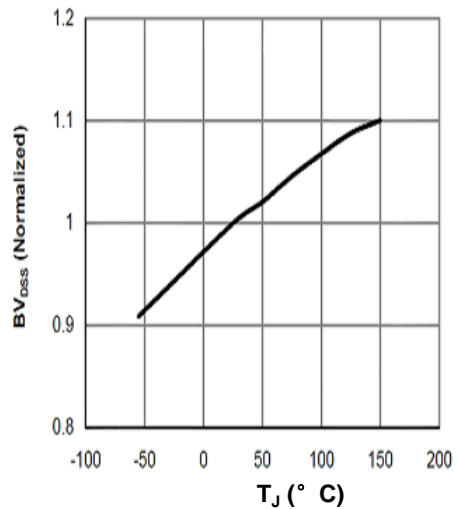


Figure 6: Break Down vs. Junction Temperature

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Typical Performance Characteristics

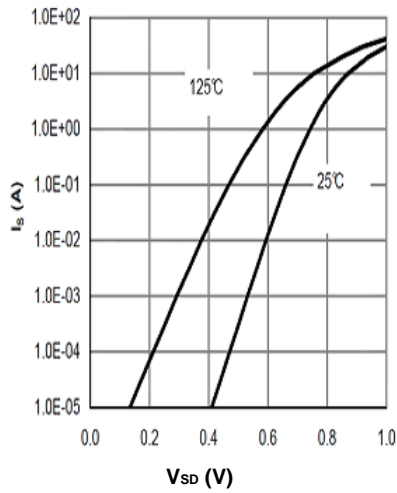


Figure 7: Body-Diode Characteristics

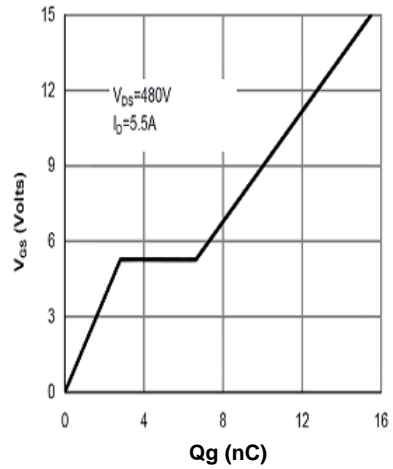


Figure 8: Gate-Charge Characteristics

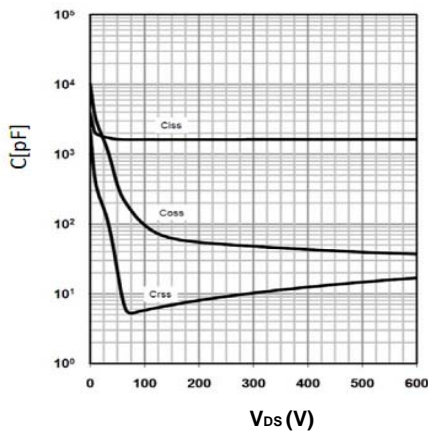


Figure 9: Capacitance Characteristics
 $C=f(V_{DS}), V_{GS}=0V, f=1MHz$

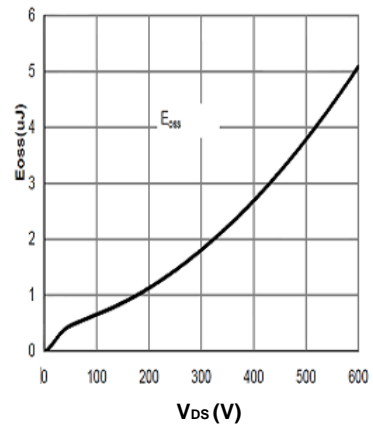


Figure 10: C_{oss} stored Energy

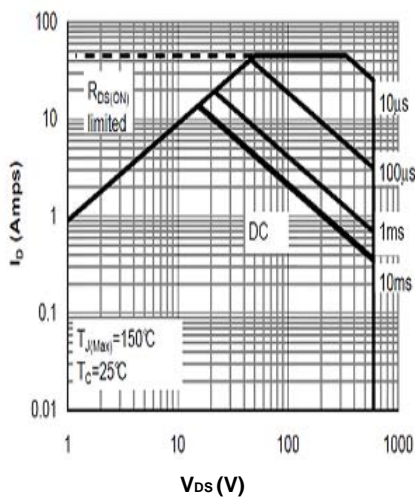


Figure 11: Maximum Forward Biased Safe Operating Area

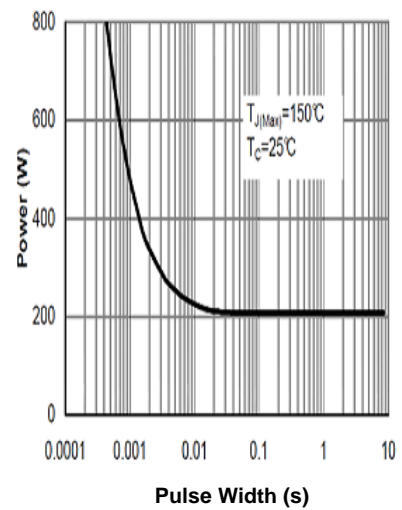


Figure 12: Single Pulse Power Rating Junction to Case

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Typical Performance Characteristics

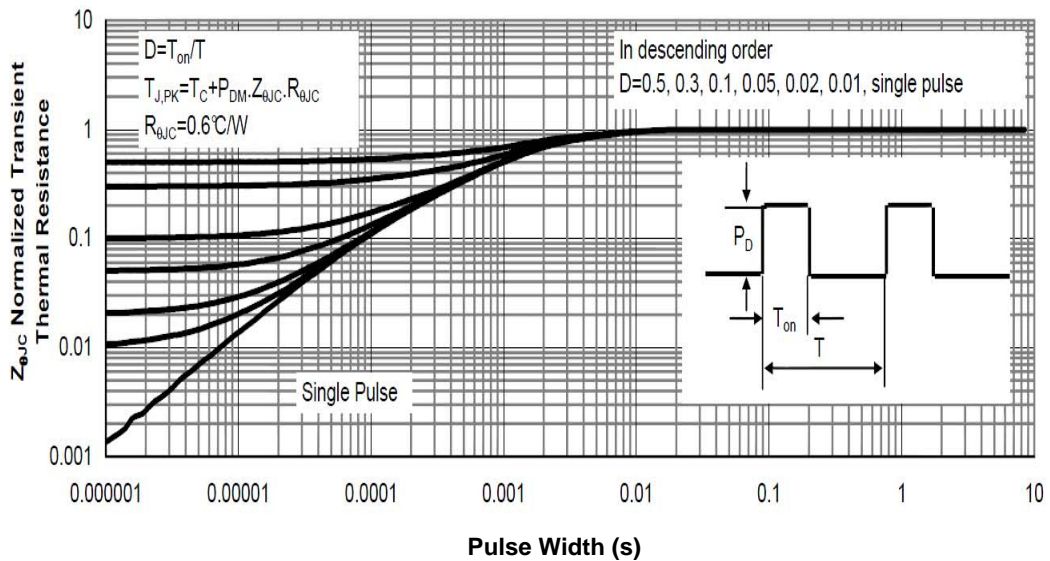


Figure 12: Normalized Maximum Transient Thermal Impedance

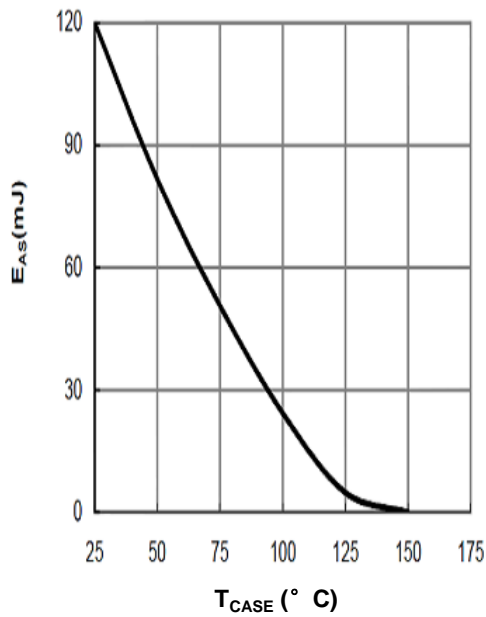


Figure 13: Avalanche energy

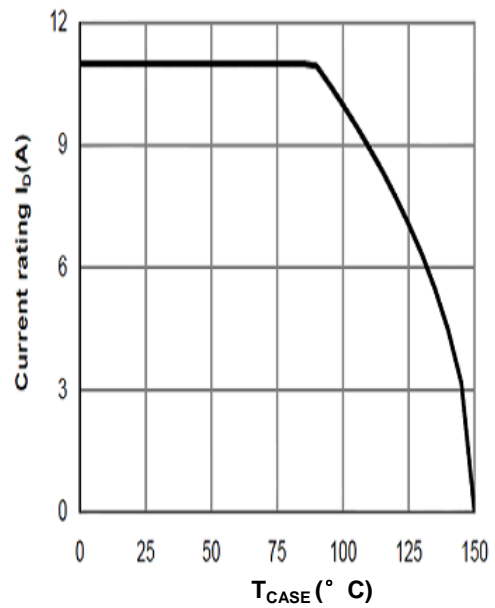


Figure 14: Current De-rating

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Typical Performance Characteristics

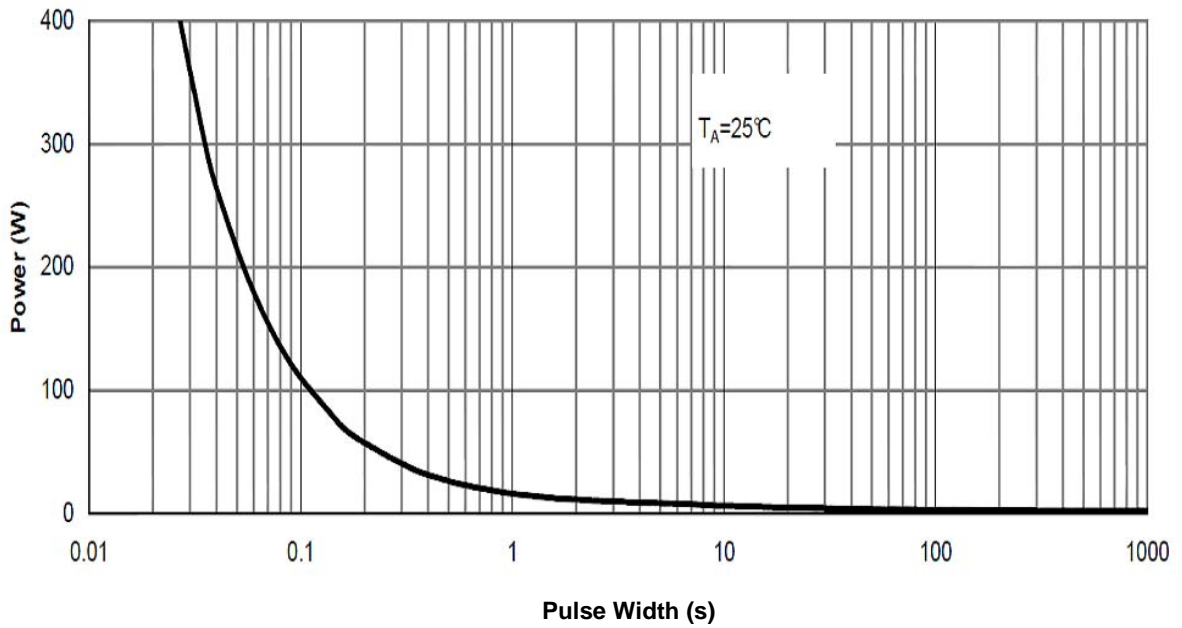


Figure 15: Single Pulse Power Rating Junction-Ambient

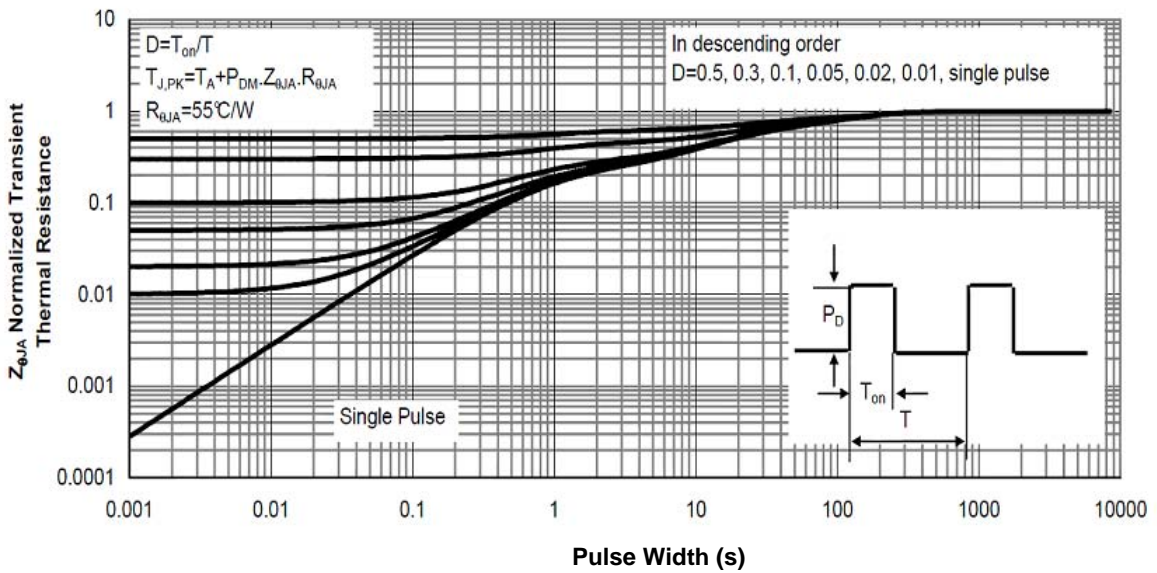
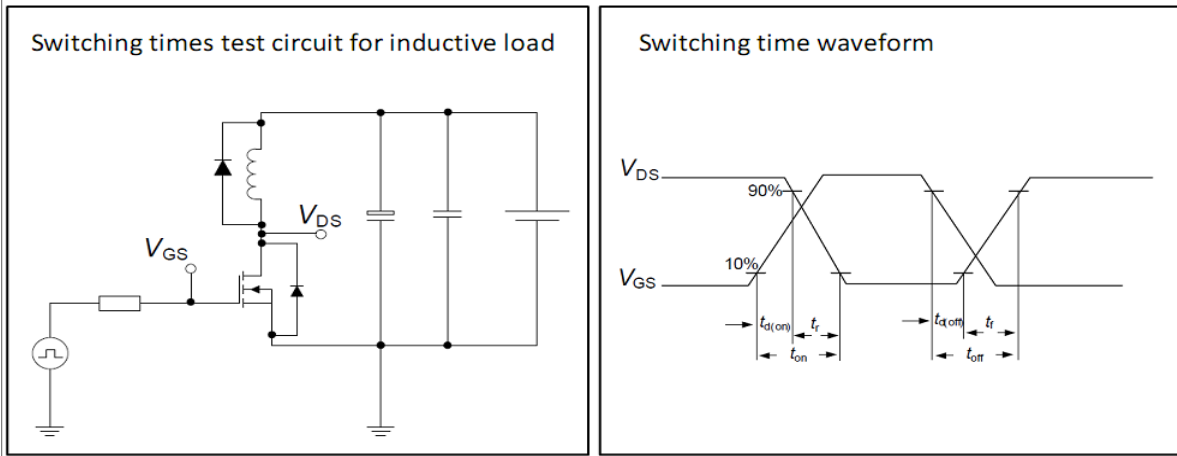


Figure 16: Normalized Maximum Transient Thermal Impedance

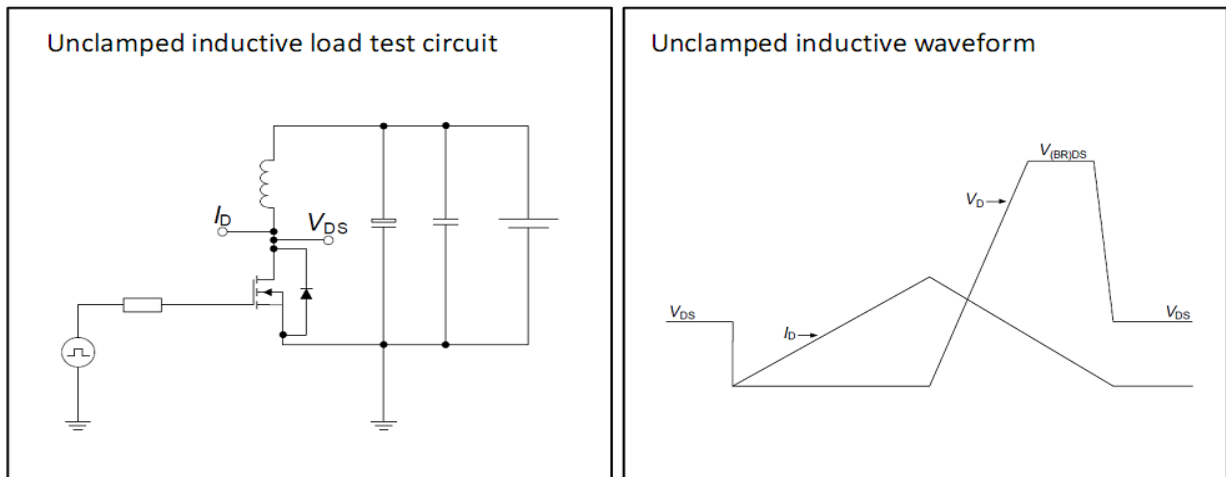
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Test circuits

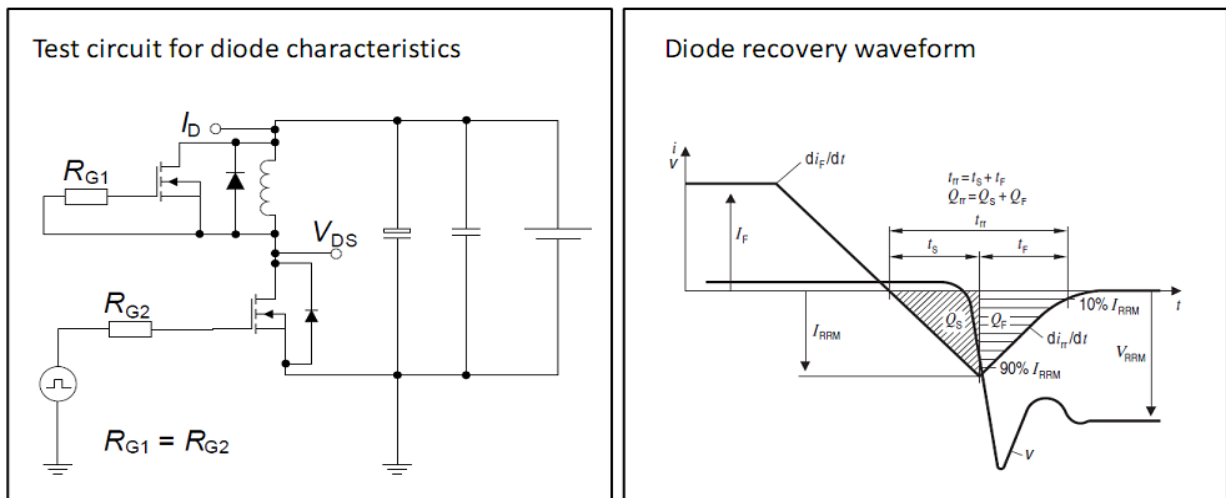
Switching times test circuit and waveform for inductive load



Unclamped inductive load test circuit and waveform



Test circuit and waveform for diode characteristics



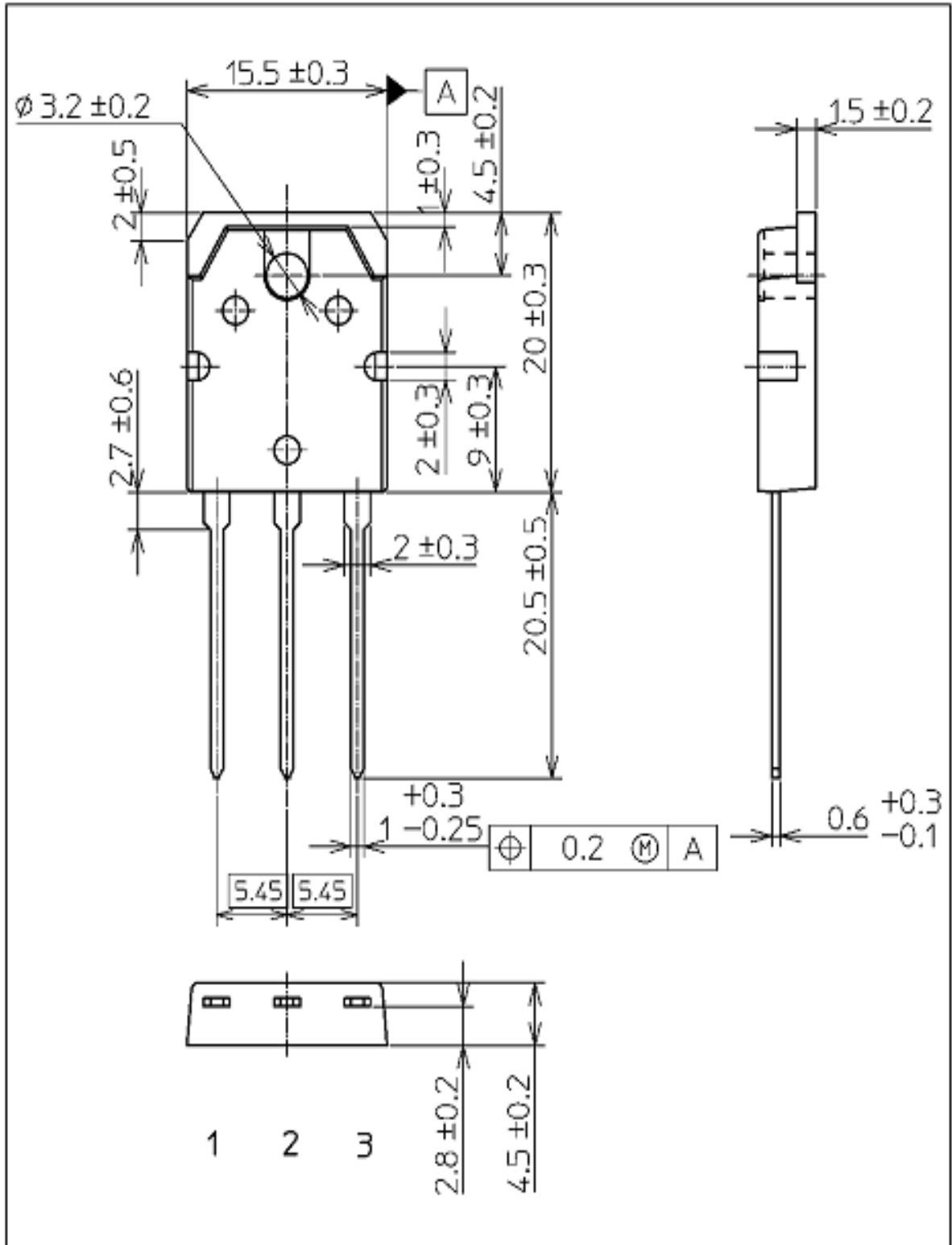
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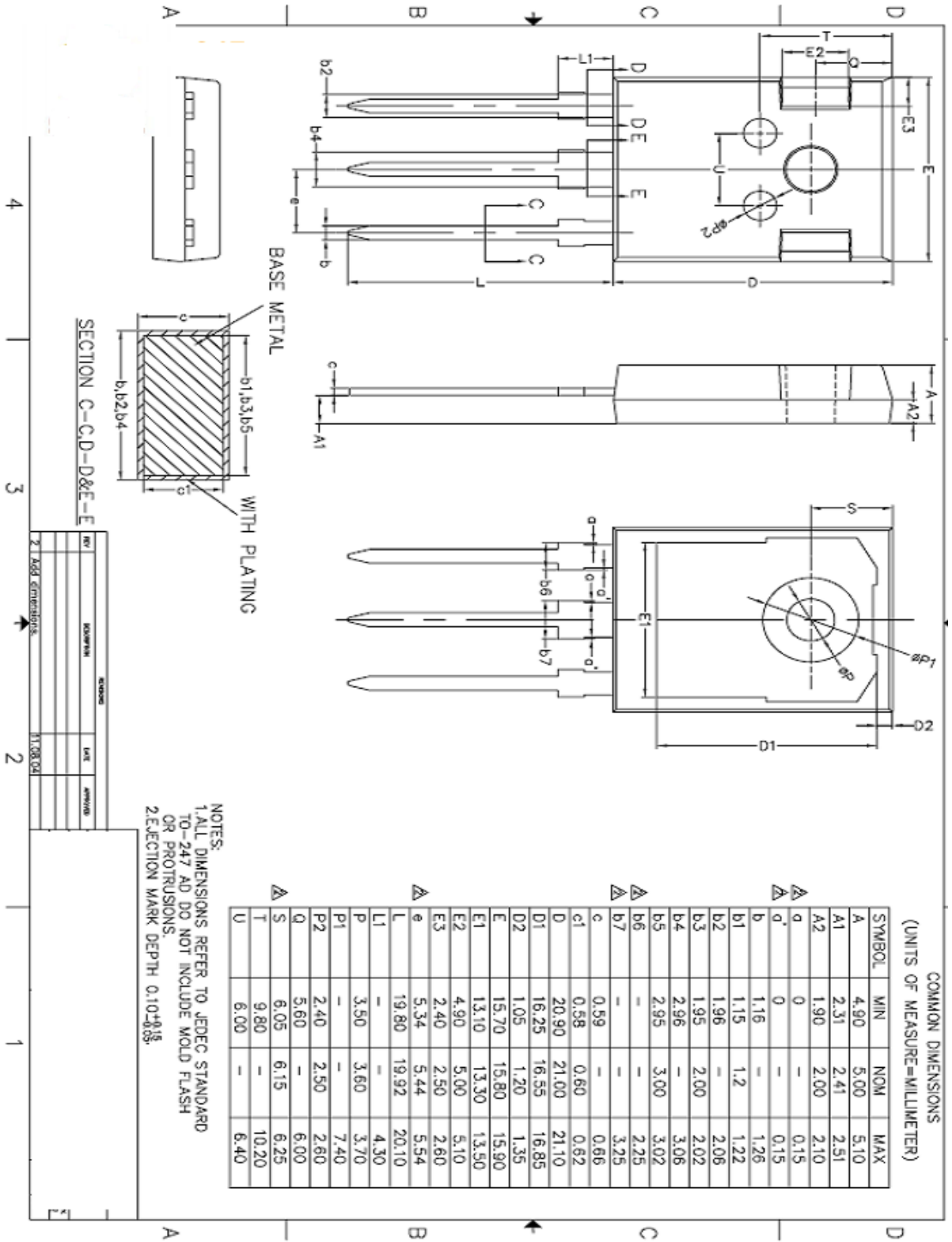
PKG TO-3P

Unit: mm



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COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
q	0	-	0.15
q'	0	-	0.15
b	1.16	-	1.26
b1	1.15	1.2	1.22
b2	1.96	-	2.06
b3	1.95	2.00	2.02
b4	2.96	-	3.06
b5	2.95	3.00	3.02
b6	-	-	2.25
b7	-	-	3.25
c	0.59	-	0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.20	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.90	5.00	5.10
E3	2.40	2.50	2.60
e	5.34	5.44	5.54
L	19.80	19.92	20.10
L1	-	-	4.30
P	3.50	3.60	3.70
P1	-	-	7.40
P2	2.40	2.50	2.60
Q	5.60	-	6.00
S	6.05	6.15	6.25
T	9.80	-	10.20
U	6.00	-	6.40

NOTES:
1. ALL DIMENSIONS REFER TO JEDEC STANDARD TO-247 AD DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
2. SECTION MARK DEPTH 0.10±0.05

REV	DESCRIPTION	DATE	APPROVED
1	ADD DIMENSIONS	11/08/04	